Ref #	Hits	Search Query	DBs	Default Operator	Plurals	Time Stamp
L1	6	(Josh near2 Mastronarde).in.	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2005/06/07 12:58
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L16	59	11 and 15	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2005/06/07 13:01
L17	7	14 and 16	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2005/06/07 13:01
L18	25043	"711"/$.ccls.	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2005/06/07 13:02
L19	362	9 and 18	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2005/06/07 13:02

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1 HW/SW co-design: Automatic generation of bus functional models from transaction level models
Dongwan Shin, Samar Abdi, Daniel D. Gajski
January 2004
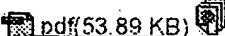
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Publisher Site

This paper presents methodology and algorithms for generating bus functional models from transaction level models in system level design. Transaction level models are often used by designers for prototyping the bus functional architecture of the system. Being at a higher level of abstraction gives transaction level models the unique advantage of high simulation speed. This means that the designer can explore several bus functional architectures before choosing the optimal one. However, the proce ...

2 Adaptive History-Based Memory Schedulers
Ibrahim Hur, Calvin Lin
December 2004 **Proceedings of the 37th annual International Symposium on Microarchitecture**

Full text available: 📄 pdf(220.26 KB)   Additional Information: full citation, abstract

As memory performance becomes increasingly important to overall system performance, the need to carefully schedule memory operations also increases. This paper presents a new approach to memory scheduling that considers the history of recently scheduled operations. This history-based approach provides two conceptual advantages: (1) it allows the scheduler to better reason about the delays associated with its scheduling decisions, and (2) it allows the scheduler to select operations so that they ...

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